

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): <b>WALLACE, Raymond C.</b>	Group Arit Unit: <b>2841</b>
Application No.: <b>10/657,457</b>	Examiner: <b>SEMENENKO, Yuriy</b>
Filed: <b>September 8, 2003</b>	Notice of Allowance: <b>November 14, 2006</b>
Title: <b>SYSTEM AND METHOD FOR MODIFYING ELECTRICAL CHARACTERISTICS</b>	

TRANSMITTAL OF FORMAL DRAWINGS

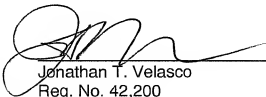
Commissioner for Patents  
Official Draftsperson  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir/Madam:

Enclosed, please find one (1) sheet of a formal drawing for the above-identified application.

Respectfully Submitted;

Dated: Dec 14, 2006

  
Jonathan T. Velasco  
Reg. No. 42,200

Jonathan T. Velasco  
Kyocera Wireless Corp.  
Attn: Patent Department  
P.O. Box 928289  
San Diego, California 92192-8289  
Tel: (858) 882-3501  
Fax: (858) 882-2485